3. Reasoning as Memory

Introduction

Item memory

Relational memory

Program memory

Introduction

Memory is part of intelligence

- Memory is the ability to store, retain and recall information
- Brain memory stores items, events and high-level structures
- Computer memory stores data and temporary variables





Memory-reasoning analogy

- 2 processes: fast-slow • Memory: familiarity-recollection
- Cognitive test:
 - Corresponding reasoning and memorization performance
 - Increasing # premises, inductive/deductive reasoning is affected



Common memory activities

- Encode: write information to the memory, often requiring compression capability
- Retain: keep the information overtime. This is often assumed in machinery memory
- Retrieve: read information from the memory to solve the task at hand

Encode

Retain

Retrieve

Memory taxonomy based on memory content



Item memory

Associative memory

RAM-like memory

Independent memory

Distributed item memory as associative memory





Rule-based reasoning with associative memory

- Encode a set of rules: "pre-conditions
- \rightarrow post-conditions"
- Support variable binding, rule-conflict handling and partial rule input
- Example of encoding rule "A:1,B:3,C:4→X"



Memory-augmented neural networks: computation-storage separation



Neural Turing Machine (NTM)

- Memory is a 2d matrix
- Controller is a neural network
- The controller read/writes to memory at certain addresses.
- Trained end-to-end, differentiable
- Simulate Turing Machine
- \rightarrow support symbolic reasoning, algorithm solving



Addressing mechanism in NTM



Optimal memory writing for memorization

- Simple finding: writing too often deteriorates memory content (not retainable)
- Given input sequence of length T and only D writes, when should we write to the memory?



Theorem 3. Given D memory slots, a sequence with length T, a decay rate $0 < \lambda \leq 1$, then the optimal intervals $\{l_i \in \mathbb{R}^+\}_{i=1}^{D+1}$ satisfying $T = \sum_{i=1}^{D+1} l_i$ such that the lower bound on the average contribution $I_{\lambda} = \frac{C}{T} \sum_{i=1}^{D+1} f_{\lambda}(l_i)$ is maximized are the following:

$$l_1 = l_2 = \dots = l_{D+1} = \frac{T}{D+1}$$

Uniform writing is optimal for memorization

Le, Hung, Truyen Tran, and Svetha Venkatesh. "Learning to Remember More with Less Memorization." In International Conference on Learning Representations. 2018.

(7)

Better memorization means better algorithmic reasoning



Memory of independent entities

- Each slot store one or some entities
- Memory writing is done separately for each memory slot
- →each slot maintains the life of one or more entities

Weston, Jason, Bordes, Antoine, Chopra, Sumit, and Mikolov, Tomas. Towards ai-complete question answering: A set of prerequisite toy tasks. CoRR, abs/1502.05698, 2015.

Task 3: Three Supporting Facts

John picked up the apple. John went to the office. John went to the kitchen. John dropped the apple. Where was the apple before the kitchen? A:office

• The memory is a set of N parallel RNNs



Recurrent entity network



Recurrent Independent Mechanisms



Relational memory

Graph memory

Tensor memory

Motivation for relational memory: item memory is weak at recognizing relationships



- Store and retrieve individual items
- Relate pair of items of the same time step
- Fail to relate temporally distant items

$$\hat{\mathbf{M}} = \sum_{k=1}^{q} \mathbf{b}_{k} \mathbf{a}_{k}^{T}$$



Dual process in memory



Howard Eichenbaum, Memory, amnesia, and the hippocampal system (MIT press, 1993).

Alex Konkel and Neal J Cohen, "Relational memory and the hippocampus: representations and methods", Frontiers in neuroscience 3 (2009).

Memory as graph

- Memory is a static graph with fixed nodes and edges
- Relationship is somehow known
- Each memory node stores the state of the graph's node
- Write to node via message passing
- Read from node via MLP



bAbl

CLEVER



Memory of graphs access conditioned on query

- Encode multiple graphs, each graph is stored in a set of memory row
- For each graph, the controller read/write to the memory:
 - Read uses content-based attention
 - Write use message passing
- Aggregate read vectors from all graphs to create output



Capturing relationship can be done via memory slot interactions using attention

- Graph memory needs customization to an explicit design of nodes and edges
- Can we automatically learns structure with a 2d tensor memory?
- Capture relationship: each slot interacts with all other slots (self-attention)



Santoro, Adam, Ryan Faulkner, David Raposo, Jack Rae, Mike Chrzanowski, Théophane Weber, Daan Wierstra, Oriol Vinyals, Razvan Pascanu, and Timothy Lillicrap. "Relational recurrent neural networks." In *Proceedings of the 32nd International Conference on Neural Information Processing Systems*, pp. 7310-7321. 2018.

Relational Memory Core (RMC) operation



row/memory-wise MLP with layer normalisation

 $s_{i,t+1} = (m_{i,t}, h_{i,t})$

Allowing pair-wise interactions can answer questions on temporal relationship



(c) Reference vector comes in the middle of a sequence, e.g. "Choose the 6th furthest vector from vector 6"

Dot product attention works for simple relationship, but ...



Self-attentive associative memory



Le, Hung, Truyen Tran, and Svetha Venkatesh. "Self-attentive associative memory." In International Conference on Machine Learning, pp. 5682-5691. PMLR, 2020.

Complicated relationship needs high-order relational memory



Program memory

Module memory

Stored-program memory

Predefining program for subtask

- A program designed for a task becomes a module
- Parse a question to module layout (order of program execution)
- Learn the weight of each module to master the task



Program selection is based on parser, others are end2end trained



The most powerful memory is one that stores both program and data

- Computer architecture: Universal Turing Machines/Harvard/VNM
- Stored-program principle
- Break a big task into subtasks, each can be handled by a TM/single purposed program
 - stored in a program memory



NUTM: Learn to select program (neural weight) via program attention

- Neural stored-program memory (NSM) stores key (the address) and values (the weight)
- The weight is selected and loaded to the controller of NTM
- The stored NTM weights and the weight of the NUTM is learnt end-to-end by backpropagation



Le, Hung, Truyen Tran, and Svetha Venkatesh. "Neural Stored-program Memory." In International Conference on Learning Representations. 2019.

Scaling with memory of mini-programs

- Prior, 1 program = 1 neural network (millions of parameters)
- Parameter inefficiency since the programs do not share common parameters
- Solution: store sharable miniprograms to compose infinite number of programs



it is analogous to building Lego structures corresponding to inputs from basic Lego bricks.
Recurrent program attention to retrieve singular components of a program



Le, Hung, and Svetha Venkatesh. "Neurocoder: Learning General-Purpose Computation Using Stored Neural Programs." arXiv preprint arXiv:2009.11443 (2020).





10. Combinatorics reasoning

RNN

MANN

GNN

Transformer

Implement combinatorial algorithms with neural networks

Train neural processor P to imitate algorithm A

Abstract outputs Processor Abstract inputs Generalizable Inflexible $A(\bar{x})$ \bar{x} Natural outputs Natural inputs Noisy **High dimensional** xy

Processor P:

- (a) aligned with the computations of the target algorithm;
- (b) operates by matrix multiplications, hence natively admits useful gradients;
- (c) operates over highdimensional latent spaces

Processor as RNN

- Do not assume knowing the structure of the input, input as a sequence
- →not really reasonable, harder to generalize
- RNN is Turing-complete → can simulate any algorithm
- But, it is not easy to learn the simulation from data (input-output)
- \rightarrow Pointer network

$$u_j^i = v^T \tanh(W_1 e_j + W_2 d_i) \quad j \in (1, \dots, n)$$

$$p(C_i | C_1, \dots, C_{i-1}, \mathcal{P}) = \operatorname{softmax}(u^i)$$



Assume O(N) memory And O(N^2) computation N is the size of input

Vinyals, Oriol, Meire Fortunato, and Navdeep Jaitly. "Pointer networks."

In Proceedings of the 28th International Conference on Neural Information Processing Systems-Volume 2, pp. 2692-2700. 2015.

Processor as MANN

- MANN simulates neural computers or Turing machine→ ideal for implement algorithms
- Sequential input, no assumption on input structure
- Assume O(1) memory and O(N) computation







Graves, A., Wayne, G., Reynolds, M. et al. Hybrid computing using a neural network with dynamic external memory. Nature 538, 471–476 (2016)

NUTM: implementing multiple algorithms at once



Le, Hung, Truyen Tran, and Svetha Venkatesh. "Neural Stored-program Memory." In International Conference on Learning Representations. 2019.

STM: relational memory for graph reasoning



Figure 2. Learning curves on algorithmic synthetic tasks.

Model	#Parameters	Convex hull		TSP		Shortest	Minimum
		N = 5	N = 10	N = 5	N = 10	path	spanning tree
LSTM	4.5 M	89.15	82.24	73.15 (2.06)	62.13 (3.19)	72.38	80.11
ALSTM	3.7 M	89.92	85.22	71.79 (2.05)	55.51 (3.21)	76.70	73.40
DNC	1.9 M	89.42	79.47	73.24 (2.05)	61.53 (3.17)	83.59	82.24
RMC	2.8 M	93.72	81.23	72.83 (2.05)	37.93 (3.79)	66.71	74.98
STM	1.9 M	96.85	91.88	73.96 (2.05)	69.43 (3.03)	93.43	94.77

Table 3. Prediction accuracy (%) for geometry and graph reasoning tasks with random *one-hot* associated features. Italic numbers are tour length–additional metric for TSP. Average optimal tour lengths found by brute-force search for N = 5 and 10 are 2.05 and 2.88, respectively.



Encoder-Process-Decode framework:

Attention

47

Example: GNN for a specific problem (DNF counting)

- Count #assignments that satisfy disjuntive normal form (DNF) formula
- Classical algorithm is P-hard O(mn)
- m: #clauses, n: #variables
- Supervised training





In Proceedings of the AAAI Conference on Artificial Intelligence, vol. 34, no. 04, pp. 3097-3104. 2020.

Example: GNN trained with reinforcement learning (maximum common subgraph)

- Maximum common subgraph (MCS) is NP-hard
- Search for MCS:
 - BFS then pruning
 - Which node to visit first?
- Cast to RL:
 - State:
 - Current subgraph
 - Node-node mapping
 - Input graph
 - Action: Node pair or edge will be visited
 - Reward: +1 if a node pair is selected
 - Q(s,a)=largest common subgraph size



Bai, Yunsheng, Derek Xu, Alex Wang, Ken Gu, Xueqing Wu, Agustin Marinovic, Christopher Ro, Yizhou Sun, and Wei Wang.

"Fast detection of maximum common subgraph via deep q-learning." arXiv preprint arXiv:2002.03129 (2020).

Learning state representation with GNN

Bidomain representation

$$h_{D_{k}} = \text{INTERACT}(\text{READOUT}(\{h_{i} | i \in \mathcal{V}_{k1}'\}), \text{READOUT}(\{h_{j} | j \in \mathcal{V}_{k2}'\})).$$

$$CT(h_{s1}, h_{s2}), h_{D_{c}}, h_{D_{0}})).$$
READOUT $(\{h_{D_{k}} | k \in \mathcal{D}^{(c)}\})$
expert estimation
$$Method \qquad ROAD \quad DBEN$$

Method	ROAD	DBEN
GLSEARCH (no $h_{\mathcal{G}}$)	0.977	0.878
GLSEARCH (no h_s)	1.000	0.874
GLSEARCH (no $h_{\mathcal{D}c}$)	0.803	0.780
GLSEARCH (no $h_{\mathcal{D}0}$)	0.576	0.856
GLSEARCH (SUM interact)	0.902	0.913
GLSEARCH (unfactored)	0.447	0.807
GLSEARCH (unfactored-i)	0.500	0.789
GLSEARCH	0.992	1.000
BEST SOLUTION SIZE	132	508

 $Q^*(s_t, a_t)$, as $r_t + \gamma V^*(s_t)$

 $Q(s_t, a_t) = 1 + \gamma \text{MLP} \Big(\mathbf{C} \Big)$

INTERA

Pretrain with ground-truth Q or Then train as DQN





Neural networks and algorithms alignment



GNN is aligned with Dynamic Programming (DP)



100%

GNN3

95%

GNN1

96%

Deep

Sets

9%

MLP

100%

Sorted

MLP

If alignment exists \rightarrow step-by-step supervision

- Merely simulate the
- classical graph algorithm, generalizable
- No algorithm discovery



Algorithm	Inputs	Supervision signals		
Breadth-first search	$x_i^{(t)}$: is <i>i</i> reachable from <i>s</i> in $\leq t$ hops?	$x_i^{(t+1)}$, $ au^{(t)}$: has the algorithm terminated?		
Bellman-Ford	$x_i^{(t)}$: shortest distance from s to i (using $\leq t$ hops)	$ \begin{array}{l} x_i^{(t+1)}, \\ \tau^{(t)}, \\ p_i^{(t)}: \mbox{ predecessor of } i \mbox{ in the} \\ \mbox{ shortest path tree (in $\leq t$ hops)} \end{array} $		Joint training is encouraged
Prim's algorithm	$x_i^{(t)}$: is node <i>i</i> in the (partial) MST (built from <i>s</i> after <i>t</i> steps)?	$x_i^{(t+1)},$ $ au^{(t)},$ $p_i^{(t)}$: predecessor of <i>i</i> in the partial MST		

Veličković, Petar, Rex Ying, Matilde Padovano, Raia Hadsell, and Charles Blundell. "Neural Execution of Graph Algorithms." In International Conference on Learning Representations. 2019.

Table 1: Accuracy of predicting reachability at different test-set sizes, trained on graphs of 20 nodes. GAT* correspond to the best GAT setup as per Section 3 (GAT-full using the full graph).

	Reachability (mean step accuracy / last-step accuracy)			
Model	20 nodes	50 nodes	100 nodes	
LSTM (Hochreiter & Schmidhuber, 1997)	81.97% / 82.29%	88.35% / 91.49%	68.19% / 63.37%	
GAT* (Veličković et al., 2018) GAT-full* (Vaswani et al., 2017)	93.28% / 99.86% 78.40% / 77.86%	93.97% / 100.0% 85.76% / 91.83%	92.34% / 99.97% 88.98% / 91.51%	
MPNN-mean (Gilmer et al., 2017) MPNN-sum (Gilmer et al., 2017) MPNN-max (Gilmer et al., 2017)	100.0% / 100.0% 99.66% / 100.0% 100.0% / 100.0%	61.05% / 57.89% 94.25% / 100.0% 100.0% / 100.0%	27.17% / 21.40% 94.72% / 98.63% 99.92% / 99.80%	



Figure 3: The per-step algorithm execution performances in terms of reachability accuracy (**left**), distance mean-squared error (**middle**) and predecessor accuracy (**right**), tested on 100-node graphs after training on 20-node graphs. Please mind the scale of the MSE plot.

Processor as Transformer

- Back to input sequence (set), but stronger generalization
- Transformer with encoder mask
 ~ graph attention
- Use Transformer with:
 - Binary representation of numbers
 - Dynamic conditional masking



Training with execution trace



The results show strong generalization



Figure 3: Sorting performance of transformers trained on sequences of up to length 8.



Figure 4: Visualizing decoder attention weights. Attention is over each row. Transformer attention saturates as the output sequence length increases, while NEE maintains sharp attention.



Figure 5: 3D PCA visualization of learned bitwise embeddings for different numeric tasks. The embeddings exhibit regular, task-dependent structure, even when most numbers have not been seen in training (c).

